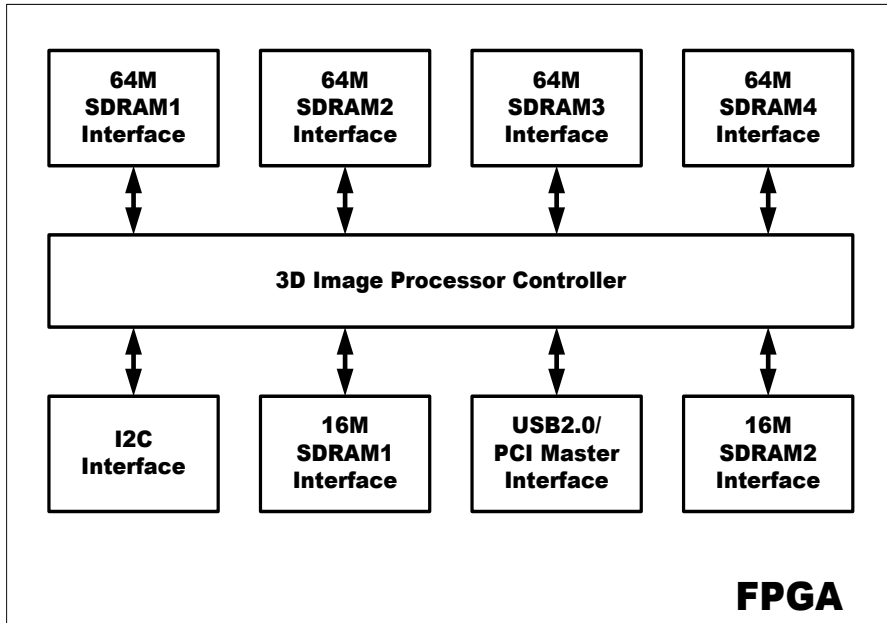
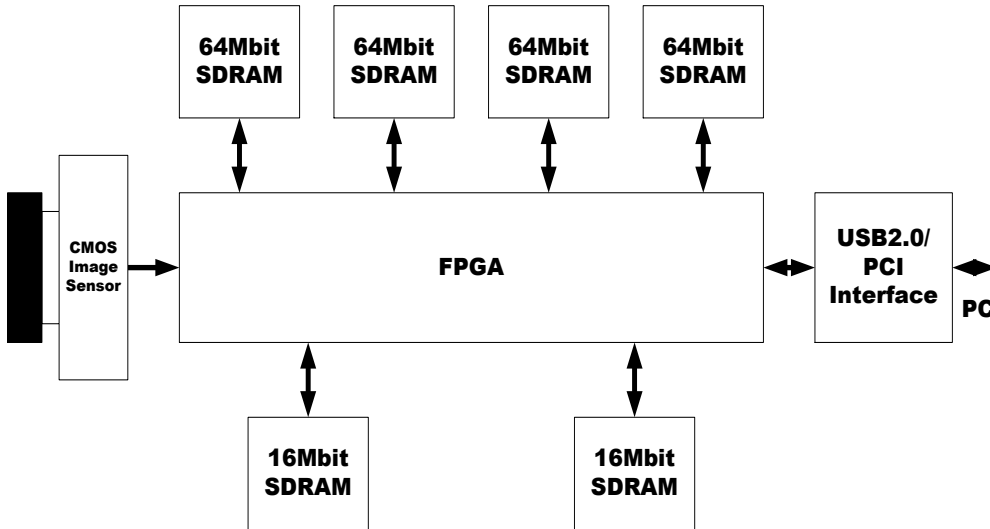


3D Image Processor Prototype Board



➤ System Spec.

FPGA : Xilinx 6,000,000gate

SDRAM : 64MB*4, 16MB*2

USB2.0 Interface

CMOS Image Sensor : 500F/S

➤ Operation flow

- 1. CMOS Image Sensor**
- 2. 500Frame SDRAM store**
- 3. 500Frame 3D processing & 500Frame store.**
- 4. 30Frame Transfer to PC**