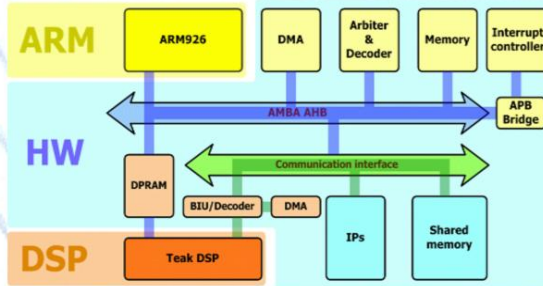


Multi-Processor System On a Chip Platform

DVB-T Baseband 수신기를 위한 멀티프로세서 SoC 플랫폼 기반 HW/SW 통합 설계

지원기관 : ETRI, 2005

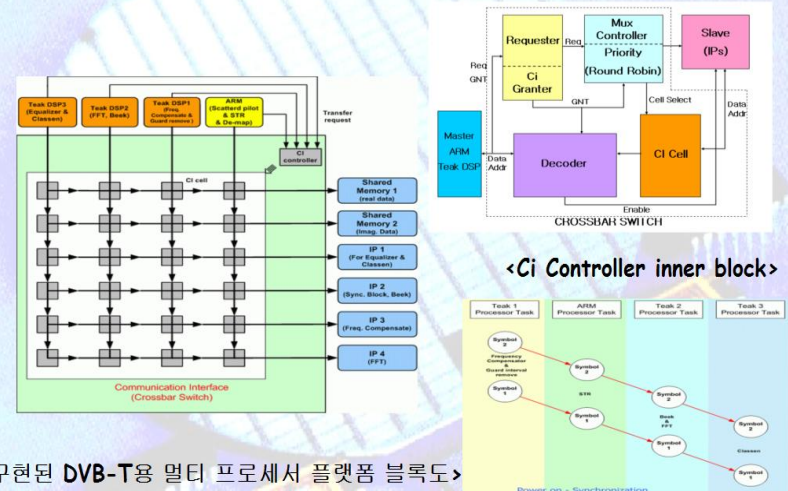


[Multi-Processor System On a Chip Platform]

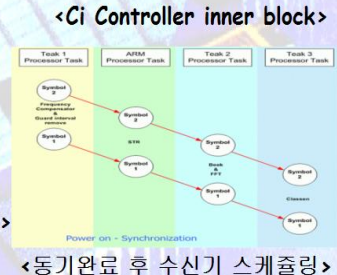
- H/W & S/W Co-Design Platform w/ Heterogeneous Components (ARM9, Teak DSP, FPGA)
- DVB-T Performance Measurement.
- ARM + DSP + AMBA Bus + Communication Interface
- H/W & S/W Co-Simulation with Mento Graphics Seamless Tool

SoC 설계 자동화 연구실

DVB-T용 멀티 프로세서 플랫폼 블록도



<구현된 DVB-T용 멀티 프로세서 플랫폼 블록도>



<동기완료 후 수신기 스케줄링>

SoC 설계 자동화 연구실

